P32031

Rev. 5/30/01 Effective March 1998

## DECLARATION AND POWER OF ATTORNEY FOR U.S. PATENT APPLICATION

(X) Origina	() Supplemental () Substitute (	) PCT (	) Design	
As a below named invento my name; that I verily believe that I am the inventor (if plural inventors are named below	or, I hereby declare that: my residence, p original, first and sole inventor (if only of the subject matter which is claimed a	one name is	s listed below) or an o	original, first and joint
Title: SEMICONDUCTOR DEVICE AND R	ESIN BINDER FOR ASSEMBLING SE	EMICONDI	JCTOR DEVICE	-
of which is described and claimed in: (X) the attached specification, or () the specification in the application Serial and with amendments through () the specification in International Application  I hereby state that I have reviewed and undersamendment(s) referred to above.  I acknowledge my duty to disclose to the Pate in Title 37, Code of Federal Regulations, §1.5	on No. PCT/	, filed specificatio	n, including the clain	ns, as amended by any
I hereby claim priority benefits under Title 35 patent or inventor's certificate listed below an before that of the application on which priorit	d have also identified below any applica	this applica	tion is for a Design) ent or inventor's certif	of any application(s) for ficate having a filing date
COUNTRY	APPLICATION NO.	DA	TE OF FILING	PRIORITY CLAIMED
Japan	2002-310736	O	ctober 25, 2002	YES
Japan	2002-310737	Oc	ctober 25, 2002	YES
I hereby claim the benefit under Title 35, Uni matter of each of the claims of this application paragraph of Title 35, United States Code \$11 Code of Federal Regulations, \$1.56 which oc date of this application.	on is not disclosed in the prior United 5 2, I acknowledge the duty to disclose in	States applice formation n	cation in the manner naterial to patentability	provided by the first ty as defined in Title 37,
APPLICATION SERIAL NO.	APPLICATION SERIAL NO. U.S. FILING DATE		STATUS: PATENTED, PENDING, ABANDONED	
		1		11

And I hereby appoint Michael R. Davis, Reg. No. 25,134; Matthew M. Jacob, Reg. No. 25,154; Warren M. Cheek, Jr., Reg. No. 33,367; Nils Pedersen, Reg. No. 33,145; Charles R. Watts, Reg. No. 33,142; and Michael S. Huppert, Reg. No. 40,268, who together constitute the firm of WENDEROTH, LIND & PONACK, L.L.P., as well as any other attorneys and agents associated with Customer No. 000513, to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith.

I hereby authorize the U.S. attorneys and agents named herein to accept and follow instructions from <u>Matsushita Electric Industrial Co., Ltd</u>, as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and myself. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by me.

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I further declare that all statements made herein of my own knowledge are true, and that all statements on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

1st Inventor	Yoshiyuki Wada	Date October 3, 2003
Yoshiy 2nd Inventor	viki Wada	Date October 3, 2003
	INU SANAI	Date
	ation may be more particularly identified as follows:	Filing Date
Applicant Reference	e Number	Atty Docket No.
Title of Invention _	SEMICONDUCTOR DEVICE AND RESIN BINDER FO	OR ASSEMBLING SEMICONDUCTOR
	DEVICE	